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Inventor Information for 10/626159

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US 20060061902 A1	US-PGPUB	20060323	Method and apparatus of dual stage servo control for track following in a hard disk drive		360/77.02				Sharma; Vinod et al.
US 20060023341 A1	US-PGPUB	20060202	METHOD AND APPARATUS FOR MICRO-ACTUATOR STROKE SENSITIVITY CALIBRATION IN A HARD DISK DRIVE		360/77.02	360/78.05; 73/865.9			Sharma; Vinod et al.
US 20060023338 A1	US-PGPUB	20060202	Head gimbal assemblies for very low flying height heads with optional micro-actuators in a hard disk drive		360/75	29/603.03; 360/250			Sharma; Vinod et al.
US 20060020316 A1	US-PGPUB	20060126	Implantable cardioversion and defibrillation system including intramural myocardial elecrtode		607/122				Martinez; Gonzalo et al.
US 20060007589 A1	US-PGPUB	20060112	METHOD AND APPARATUS OF DUAL STAGE SERVO CONTROL FOR TRACK FOLLOWING IN A HARD DISK DRIVE		360/77.02				Sharma; Vinod et al.
US 20050266576 A1	US-PGPUB	20051201	Identifying patients at risk for life threatening arrhythmias		436/86				Soykan, Orhan et al.
US 20050177196 A1	US-	20050811	Methods and		607/14	607/9			Soykan,

	PGPUB			apparatus for identifying patients at risk for life threatening arrhythmias				Orhan et al.
US 20050105218 A1	US-PGPUB	20050519		Method and apparatus controlling communication in the main flex and bridge flex circuits for multiple micro-actuators in a hard disk drive	360/245.9			Sharma, Vinod et al.
US 20050096720 A1	US-PGPUB	20050505		Implantable medical device having optical fiber for sensing electrical activity	607/122			Sharma, Vinod et al.
US 20050091914 A1	US-PGPUB	20050505		Antioxidant composition for motor gasoline	44/412			Gupta, Anurag Ateet et al.
US 20050070966 A1	US-PGPUB	20050331		Inter-episode implementation of closed loop ATP	607/14			Sharma, Vinod
US 20050021089 A1	US-PGPUB	20050127		Compositions and methods for treating cardiac dysfunction	607/2			Sharma, Vinod
US 20040267863 A1	US-PGPUB	20041230		Method and apparatus for performing single-cycle addition or subtraction and comparison in redundant form arithmetic	708/710			Bhushan, Bharat et al.
US 20040240117 A1	US-PGPUB	20041202		Operating a proximity latch mechanism using an impact	360/256.2			Chang, Joseph et al.

			rebound crash stop for an outside disk ramp loading disk drive					
US 20040231140 A1	US-PGPUB	20041125	Manufacturing method using a proximity latch mechanism in an impact rebound crash stop for an outside disk ramp loading disk drive	29/603.03	29/603.07			Chang, Joseph et al.
US 20040215251 A1	US-PGPUB	20041028	Genetic modification of targeted regions of the cardiac conduction system	607/9				Sharma, Vinod et al.
US 20040214182 A1	US-PGPUB	20041028	Genetic modification of targeted regions of the cardiac conduction system	435/6	435/320.1; 435/325; 435/69.1; 530/350; 536/23.5			Sharma, Vinod et al.
US 20040181259 A1	US-PGPUB	20040916	Method and apparatus to control delivery of high-voltage and anti-tachy pacing therapy in an implantable medical device	607/4				DeGroot, Paul J. et al.
US 20040167579 A1	US-PGPUB	20040826	Method and apparatus for generation and selection of tachycardia therapy hierarchy	607/14				Sharma, Vinod et al.
US 20040106956 A1	US-PGPUB	20040603	Apparatus and method using ATP return cycle length for	607/9				Sharma, Vinod et al.

				arrhythmia discrimination				
US 20040088017 A1	US-PGPUB	20040506		Ischemia detection based on cardiac conduction time	607/25			Sharma, Vinod et al.
US 20040066580 A1	US-PGPUB	20040408		Method and apparatus for head slider and suspension reducing air flow turbulence around the head when accessing a rotating disk in a disk drive	360/234.3			Sharma, Vinod et al.
US 20040030834 A1	US-PGPUB	20040212		Multilevel cache system and method having a merged tag array to store tags for multiple data arrays	711/122	711/128; 711/3		Sharma, Vinod
US 20030204210 A1	US-PGPUB	20031030		Combined anti-tachycardia pacing (ATP) and high voltage therapy for treating ventricular arrhythmias	607/14			Ousdigian, Kevin T. et al.
US 20030191502 A1	US-PGPUB	20031009		Field stimulation about a discontinuity of the myocardium to capture the heart at reduced pacing thresholds	607/9			Sharma, Vinod et al.
US 20030191403 A1	US-PGPUB	20031009		Method and apparatus for predicting recurring ventricular arrhythmias	600/515	600/518; 607/5		Zhou, Xiaohong et al.
US 20030189795 A1	US-	20031009		Proximity latch	360/256.2			Chang,

	PGPUB		mechanism using an impact rebound crash stop for an outside disk ramp loading disk drive			Joseph et al.
US 20030184913 A1	US-PGPUB	20031002	Ramp design for reducing read-write head track positioning errors	360/128	360/254.7	Sharma, Vinod et al.
US 20030139779 A1	US-PGPUB	20030724	Field stimulation about a discontinuity of the myocardium to capture the heart at reduced pacing thresholds	607/9		Sharma, Vinod et al.
US 20030035246 A1	US-PGPUB	20030220	Impact rebound type single lever bi-directional inertia latch	360/256.4		Byun, Yongkyu et al.
US 20030023273 A1	US-PGPUB	20030130	Method and apparatus to control delivery of high-voltage and anti-tachy pacing therapy in an implantable medical device	607/4		DeGroot, Paul J. et al.
US 20030009638 A1	US-PGPUB	20030109	METHOD AND APPARATUS FOR MAINTAINING CACHE COHERENCE IN A COMPUTER SYSTEM	711/145	711/133; 711/144	SHARMA, VINOD et al.
US 20020174731 A1	US-PGPUB	20021128	Controlled particle deposition in drives and on	73/865.6		Sharma, Vinod et al.

			media for thermal asperity studies				
US 20020174157 A1	US-PGPUB	20021121	Method and apparatus for performing equality comparison in redundant form arithmetic	708/671	708/493		Bhushan, Bharat et al.
US 20020147755 A1	US-PGPUB	20021010	Method and apparatus for a fast comparison in redundant form arithmetic	708/671			Bhushan, Bharat et al.
US 20020013800 A1	US-PGPUB	20020131	Method and apparatus for performing single-cycle addition or subtraction and comparison in redundant form arithmetic	708/493	708/670; 708/671		Bhushan, Bharat et al.
US 7012785 B2	USPAT	20060314	Method and apparatus for head slider and suspension reducing air flow turbulence about the slider when accessing a rotating disk in a disk drive	360/234.2	360/236.4		Sharma; Vinod et al.
US 7009804 B2	USPAT	20060307	Method and apparatus for micro-actuator stroke sensitivity calibration in a hard disk drive	360/77.02	360/78.05; 73/865.9		Sharma; Vinod et al.
US 7009803 B2	USPAT	20060307	Method and apparatus of dual stage servo control for track following in a hard disk drive	360/77.02			Sharma; Vinod et al.

US 6967822 B2	USPAT	20051122	Disk drive actuator impact rebound crash stop with bias tab and pusher and crash stop faces	360/265.1	360/256.2		Chang; Joseph et al.
US 6922585 B2	USPAT	20050726	Method and apparatus for predicting recurring ventricular arrhythmias	600/518	600/515; 607/19		Zhou; Xiaohong et al.
US 6892094 B2	USPAT	20050510	Combined anti-tachycardia pacing (ATP) and high voltage therapy for treating ventricular arrhythmias	607/4			Ousdigian; Kevin T. et al.
US 6857051 B2	USPAT	20050215	Method and apparatus for maintaining cache coherence in a computer system	711/145	711/144		Sharma; Vinod et al.
US 6836826 B2	USPAT	20041228	Multilevel cache system and method having a merged tag array to store tags for multiple data arrays	711/122	711/124; 711/128; 711/144; 711/146; 711/156; 711/3		Sharma; Vinod
US 6826588 B2	USPAT	20041130	Method and apparatus for a fast comparison in redundant form arithmetic	708/671			Bhushan; Bharat et al.
US 6813628 B2	USPAT	20041102	Method and apparatus for performing equality comparison in redundant form arithmetic	708/671	708/200		Bhushan; Bharat et al.

US 6763368 B2	USPAT	20040713		Method and apparatus for performing single-cycle addition or subtraction and comparison in redundant form arithmetic		708/671	708/200		Bhushan; Bharat et al.
US 6747843 B2	USPAT	20040608		Ramp design for reducing read-write head track positioning errors		360/128	360/254.4		Sharma; Vinod et al.
US 6718204 B2	USPAT	20040406		Method and apparatus to control delivery of high-voltage and anti-tachy pacing therapy in an implantable medical device		607/4	607/14		DeGroot; Paul J. et al.
US 6609209 B1	USPAT	20030819		Method and apparatus for reducing the power consumed by a processor by gating the clock signal to pipeline stages		713/322			Tiwari; Vivek et al.
US 6591341 B1	USPAT	20030708		Multilevel cache system and method having a merged tag array to store tags for multiple data arrays		711/122	711/124; 711/144; 711/146; 711/156		Sharma; Vinod
US 6564328 B1	USPAT	20030513		Microprocessor with digital power throttle		713/320			Grochowski; Edward T. et al.
US 6446517 B1	USPAT	20020910	5	Controlled particle deposition in drives and on media for		73/865.6			Sharma; Vinod et al.

				thermal asperity studies					
US 6327646 B1	USPAT	20011204		Translation look-aside buffer utilizing high-order bits for fast access		711/207	365/189.07; 365/49; 711/108; 711/205		Sharma; Vinod et al.
US 6055606 A	USPAT	20000425		Writeback cache cell with a dual ported dirty bit cell and method for operating such a cache cell		711/131	365/230.05; 711/143; 711/156		Sharma; Vinod
US 5559970 A	USPAT	19960924		Crossbar switch for multi-processor, multi-memory system for resolving port and bank contention through the use of aligners, routers, and serializers		710/317	711/148; 711/150		Sharma; Vinod
US 4268324 A	USPAT	19810519		Fabrication of spectrally selective solar surfaces by the thermal treatment of austenitic stainless steel AISI 321		428/472.1	126/908; 148/240; 428/469		Sharma; Vinod C.